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| 10/798,268 | 02/20/2004 | | Praveen K. | Samudrala | 1372.136.PRC | 2267 |
| AFPLN, TYPE | SMALL ENTITY | ISSUE F | | PUBLICATION FEE | TOTAL FEE(S) DUE | DATE DUE |
| | YES | \$700 | | \$300 | \$1000 | 09/06/2005 |
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| 74 | in the united states patent a | ND TRADEMARK OFFICE | |
| 0 & 2005 In re A | plication of: Praveen K. Samudrala |) | |
| ACCEPTANCE | No.: 10/708,268 |) Art Unit:) Examiner: | 2819 Daniel D. Chang |
| Filed: | 02/20/2004 |) Confirmation No. | 2267 |
| For: | Method and Apparatus for Creating Circuit Redundancy in Programmable Logic Devices |))) | |
| Mail Sto Commis P.O. Bo | o (703) 746-4000 op ISSUE FEE ssioner for Patents x 1450 Iria, VA 22313-1450 | | |
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